

10/01/2002
02/14/02

JC4/956

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM:	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10075218	02/14/2002	257	100+	281	FARAHAN

**APPLICANTS: Kobayashi Thomas; Sheck Stephen; Pozder Scott;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	SC11931TP
Verified and Acknowledged Examiners's initials	

TITLE : Semiconductor device having a fuse and method of forming thereof

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheet(s) Drawn	Fig(s) Drawn
ISSUE FEE		Print Fig(s)	
Amount Due	Date Paid	Application Examiner	
		PREPARED FOR ISSUE	
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

FILED WITH: ☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)